Memo

To: Micah Taylor

From: Group D Section 4

Date: November 4, 2014

Re: Milestone 5

Dear Professor,

For Milestone 5, Bo and An redrew the datapath to match the changes made. An started the datapath in Xilinx and Bo finished that. Bo also wrote the testbench of the control unit.Bo wrote the integration tests. Bo, Donai, and Jackie made changes to the testbenches. (Donai and Jachie fixed the Register File; Bo implemented a new mux(16bits 4inputs), made a new memory block(to make that to accept 16bits input) by using the memory generator and fixed the 16-bits ALU). Bo also wrote the integration test plan and changes to the signals and RTL in the design document. Donai and Jackie did the documentation for the milestone.

Sincerely,

Group 4D